



GENERAL INFORMATION				
SIMPLWINDOWS NAME:	L-ISA Processor			
CATEGORY:	MULTI-CHANNEL AUDIO PROCESSOR			
VERSION:	V1.1.0			
SUMMARY:	The module provides monitoring and control over HTTP			
GENERAL NOTES:	This module is for the control of one L-ISA Processor			
CRESTRON HARDWARE REQUIRED:	3-Series Processor, 4-Series Processors			
SETUP OF CRESTRON HARDWARE:	The Crestron Control Processor's IP address usually is in the same subnet as the L-ISA Processors (typically 192.168.1.x/255.255.255.0, but other classes are possible, see the L-ISA Processor user manual). Currently the L-SA Processor does not have an IP Gateway in its network settings. This means that the Crestron Control Processor and the L-ISA Processor (Control network interface) must be set on the same subnet.			
VENDOR FIRMWARE:	L-ISA Processor minimum firmware version: v2.4.0			
VENDOR SETUP:	L-ISA Processor connected to the Ethernet Network			

SUPPORT CONTACT			
COMPANY NAME:	L-Acoustics		
SUPPORT CONTACT:	Application, Electronics / AV Control System		
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Device Type: Surround Sound Processor



RELEASE NOTES

Version 1.1.0 (January 2023)

New features/Improvements

8007	Support 30 configuration memory slots	
7983	Levels Control (fader position and mute) for Master, Reverb, Monitoring, Fader 1 and Fader 2	
7985	Recall configurations without affecting Levels Control	

Fixed issues

n/a

Version history

Version 1.0.0 (May 2022)

Initial release.

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Note: most input signals are not effective when the L-ISA Processor is locked by L-ISA Controller software (output signal Locked_fb is HIGH). An HTTP 403 error will be raised.

CONTROL			
Connect	D	The Connect signal is used to activate the functions of the module. As soon as this signal is HIGH, the module starts the polling to the L-ISA Processor over HTTP. When the connection is successful, all other input signals are effective. When the signal is LOW, the module stops all network operations and input signals are not effective.	
Power_On Shut_Down	D	Power_On and Shut_Down react to rising edges. Note: Power_On and Shut_Down features are only compatible with L-ISA Processor II. Setting these signals HIGH turns the L-ISA Processor On or Off.	
Recall_Configuration_[XX] [XX] = 01 to 30	D	Recall_Configuration_[XX] react to rising edges. Push these signals to recall one of the existing Configuration memory slots stored inside of the L-ISA Processor.	
Mute_[YYY] [YYY] = Master, Reverb, Monitoring, Fader1 or Fader2	D	Set the mute state of the Levels Control fader. HIGH = mute LOW = unmute	
Level_[YYY]# [YYY] = Master, Reverb, Monitoring, Fader1 or Fader2	Α	Set the position of the Levels Control fader. Range: from 0d (-80.0 dB) to 65535d (0.0 db).	
Level_[YYY]_Up [YYY] = Master, Reverb, Monitoring, Fader1 or Fader2	D	These signals react to a rising edge. Increase the level of the Levels Control fader by the amount of dB determined by analog input signal Level_UpDown_Step_in_dB#.	
Level_[YYY]_Down [YYY] = Master, Reverb, Monitoring, Fader1 or Fader2	D	These signals react to a rising edge. Reduce the level of the Levels Control fader by the amount of dB determined by analog input signal Level_UpDown_Step_in_dB# .	
Level_UpDown_Step_in_dB#	Α	Amount of dB to be added to/removed from the Levels Control fader when rising digital input signals Level_[YYY]_Up and Level_[YYY]_Down . Range: from 1d (1 dB) to 12d (12 dB).	
Level_[YYY]_Safe_Recall [YYY] = Master, Reverb, Monitoring, Fader1 or Fader2	D	When these signals are set to HIGH, the level of the Levels Control fader is not affected when recalling a configuration with digital input signal Recall_Configuration_[XX] . When these signals are set to LOW, the level of the Levels Control fader is restored to the previously saved position when recalling a configuration with digital input signal Recall_Configuration_[XX] .	

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D



Mute_[YYY]_Safe_Recall

[YYY] = Master, Reverb, Monitoring, Fader1 or Fader2

When these signals are set to HIGH, the mute state of the Levels Control fader is not affected when recalling a configuration with digital input signal **Recall_Configuration_[XX]**.

When these signals are set to LOW, the mute state of the Levels Control fader is restored to the previously saved state when recalling a configuration with digital input signal **Recall_Configuration_[XX]**.





FEEDBACK		
Online_fb	D	This signal is HIGH when the L-ISA Processor is available and connected. This signal is LOW when the L-ISA Processor is not available or when the module is not connected.
Display_Name\$	S	Name of the connected L-ISA Processor
Firmware_Version\$	S	Current firmware version of the L-ISA Processor
Unique_ID\$	S	Unique ID of the connected L-ISA Processor
Locked_fb	D	HIGH = an instance of L-ISA Controller software is currently connected to the L-ISA Processor. The L-ISA Processor is locked and all operations from Crestron are disabled. LOW = the L-ISA Processor is not locked. Operations from Crestron are enabled.
Error_Present_fb	D	HIGH = an error is present in the module (see Error_Message\$ for details). LOW = no error present in the module.
Error_Message\$	S	This signal gives details on the nature of the errors present in the module or the unit. If several errors occur, they are delimited by '\r' (\x0D) carriage return characters.
Current_Configuration_Number#	Α	If a configuration is currently loaded, this signal represents its index (from 1 to 20).
		If no configuration is loaded, this signal equals 0.
Current_Configuration_Name\$	S	Name of the currently loaded configuration. Empty if no configuration is loaded.
Configuration_[XX]_Name	S	Each signal gives the name of the available configuration stored on the L-ISA Processor. When there is no valid configuration at a given index, then its name is empty.
Mute_[YYY]_fb		Current mute state of the Levels Control fader
[YYY] = Master, Reverb, Monitoring, Fader1 or Fader2	D	HIGH = muted LOW = unmuted
Level_[YYY]_fb#		Current position of the Levels Control fader
[YYY] = Master, Reverb, Monitoring, Fader1 or Fader2	Α	Range: 0d (-80.0 dB) to 65535 (0.0 dB)
Level_[YYY]_String\$ [YYY] = Master, Reverb, Monitoring, Fader1 or Fader2	S	Current level of the Levels Control fader as human-readable dB value.





PARAMETERS		
		IP address of the L-ISA Processor to connect to, for example "192.168.1.100". The IP address must be in the following ranges:
IpAddress	S	- 10.0.0.1 - 10.255.255.254 (Class A) - 172.16.0.1 - 172.31.255.254 (Class B) - 192.168.0.1 - 192.168.255.254 (Class C) - 100.64.0.1 - 100.127.255.254 (SAS) - 169.254.0.1 - 169.254.255.254 (APIPA)

TESTING	
OPS USED FOR TESTING:	RMC3 1.8001.4788.22932
SIMPL WINDOWS USED FOR TESTING:	4.1700.03
CRESTRON DB USED FOR TESTING:	209.0500.001.00
DEVICE DB USED FOR TESTING:	200.14000.001.00
SAMPLE PROGRAM:	Demo